

Faculty Profile

Name: P. Ravi Kanth

About me:

P. Ravi Kanth, Associate Professor of Electrical and Electronics Engineering, completed his B.Tech from JNTUH Hyderabad and M. E from Birla Institute Of Technology & Science, Pilani, Rajasthan. He is having two years research and over twenty years of academic experience. He has worked as Scientist Fellow in Microelectronics Area in Central Electronics Engineering & Research Institute, PILANI, Rajasthan. He is pursuing Part-Time Ph. D work in JNTU, Hyderabad. He worked as a faculty member in different academic institutions.

Contact Details:

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Courses Taught:

Switching Theory and Logic Design/ Digital Electronics/ Principles of Digital Electronics

Basic Electrical & Electronics Engineering

Integrated Circuit Analysis/ Operational Amplifiers

Electronic Devices & Circuits, Pulse & Digital Circuits, Electronic Circuit Analysis.

VLSI Design

Sponsored Projects:

Ongoing Projects: Nil

Completed Projects: Nil

Publications:**Journal Publications:**

Image Compression by Discrete Curvelet Wrapping Technique with Simplified SPHIT
By SivaNagiReddy, K.; Koteswara Rao, L.; R. Vikram, B.; Ravikanth, P. International
Journal of Computer Applications, vol. 39, issue 18, pp. 1-9. 02/2012

Research Interest: VLSI Design, LOW Power VLSI Design

Seminars:

Seminars/Workshops Attended:

Awards:

Invited Talks: Nil